

JPW

Inventors: Angelo Magri', Ferruccio Frisina

Title: Insulated Gate Planar Integrated Power Device With Co-Integrated Schottky Diode and Process

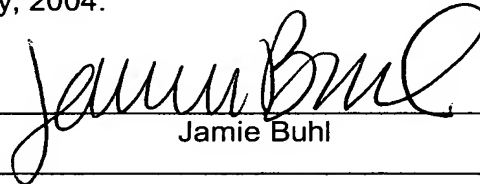
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I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail and is addressed to Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 14th day of May, 2004.



Jamie Buhl

**PRELIMINARY AMENDMENT IN RESPONSE TO NOTICE OF NON-COMPLIANT
AMENDMENT MAILED MAY 14, 2004**

May 14, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Commissioner:

Please amend the application as follows.

The amendments to the claims begin on page 2 of this Preliminary Amendment.